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## APPLICANTS

Kenichiro Uda, Osaka, JAPAN;

\*\* CONTINUING DATA *NONE* *YLR*  
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\*\* FOREIGN APPLICATIONS *YES* *YLR*  
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 JAPAN P2002-271362 09/18/2002

IF REQUIRED, FOREIGN FILING LICENSE  
 GRANTED \*\* 12/12/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Allen Koshishu</i> <i>YLR</i> Examiner's Signature Initials	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 5	<b>TOTAL CLAIMS</b> <i>12</i> 13	<b>INDEPENDENT CLAIMS</b> 1
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## TITLE

Structure of power supply path utilized in design of integrated circuit

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